FIG. 1 PRIOR ART

Low swing signaling  $CV_{dd}/(V_{dd}-V_{th})^{\alpha}$ **Conventional** CV<sub>dd</sub><sup>2</sup>f Concept Power Delay

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Internor(s):

Atsuki Inoue

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FIG. 2 PRIOR ART

	Conventional	Charge recycling
Initial state	Tep #2	+6/2 +6/2 <u> </u>
Drive		
Final		$ \begin{array}{c c}  & \alpha/2 \\  & c \\  & \beta \\  & \beta$
		Precycle = Pcon/2

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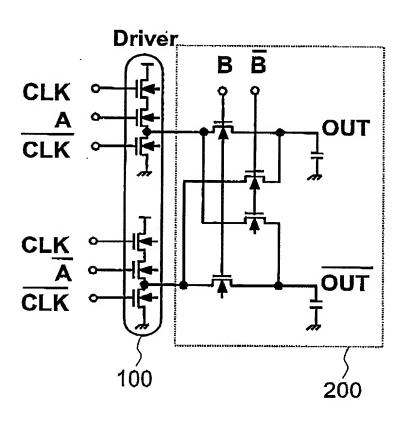
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FIG.3 PRIOR ART



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FIG.4

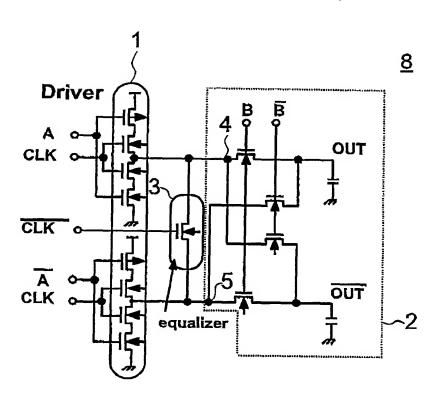
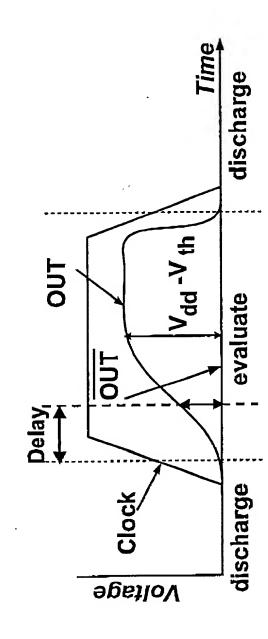


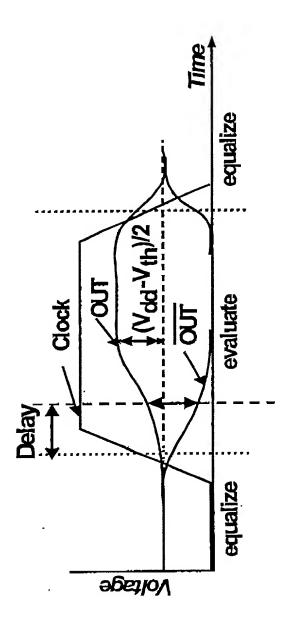
FIG. 5 PRIOR ART



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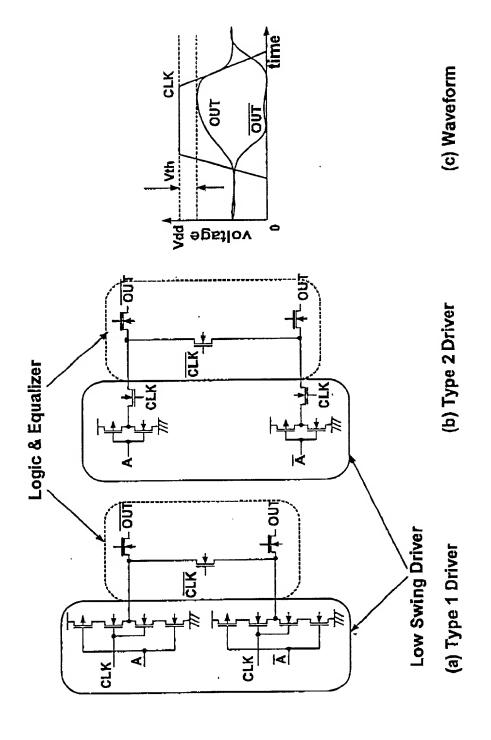
FIG. 6 PRIOR ART



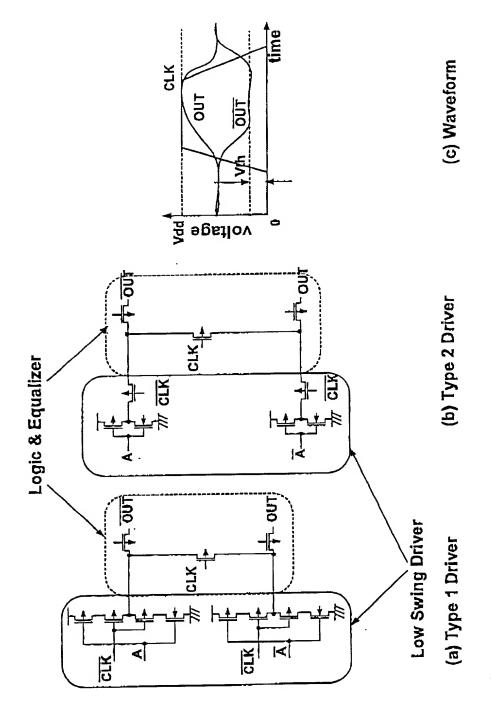
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F16. 7

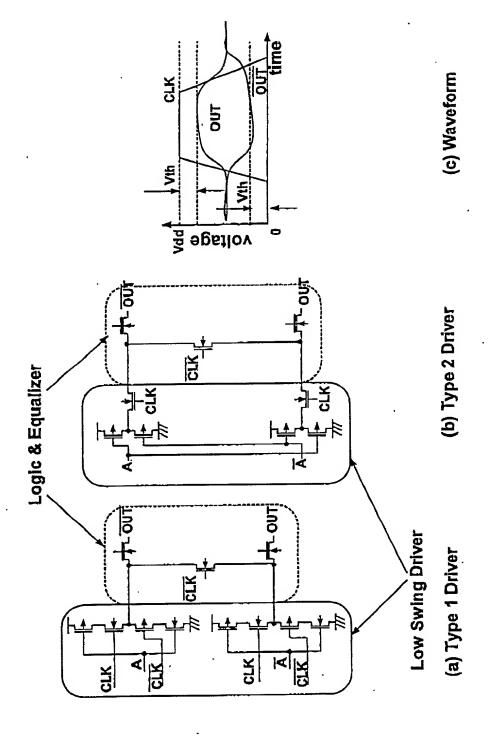


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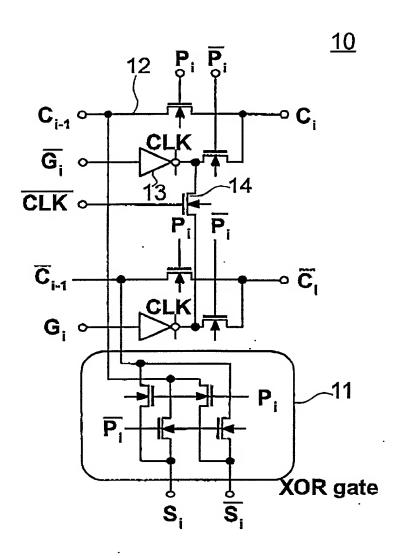
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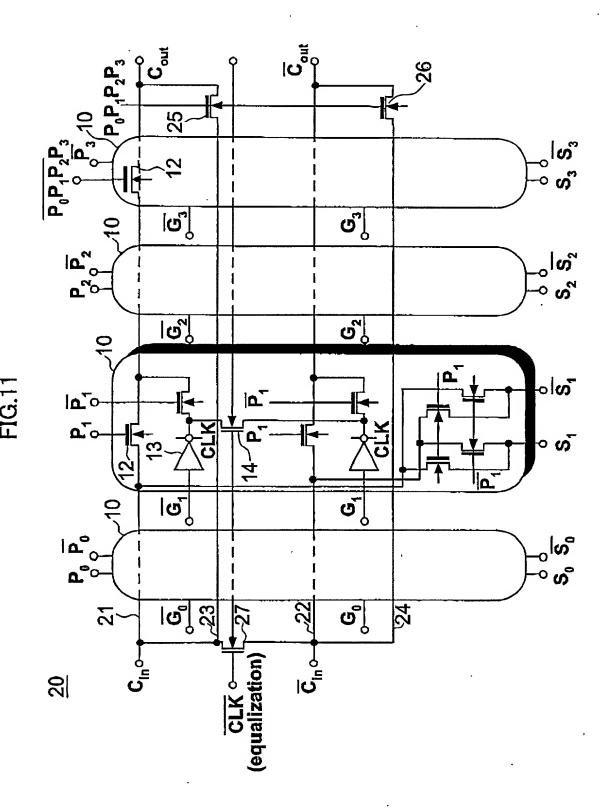


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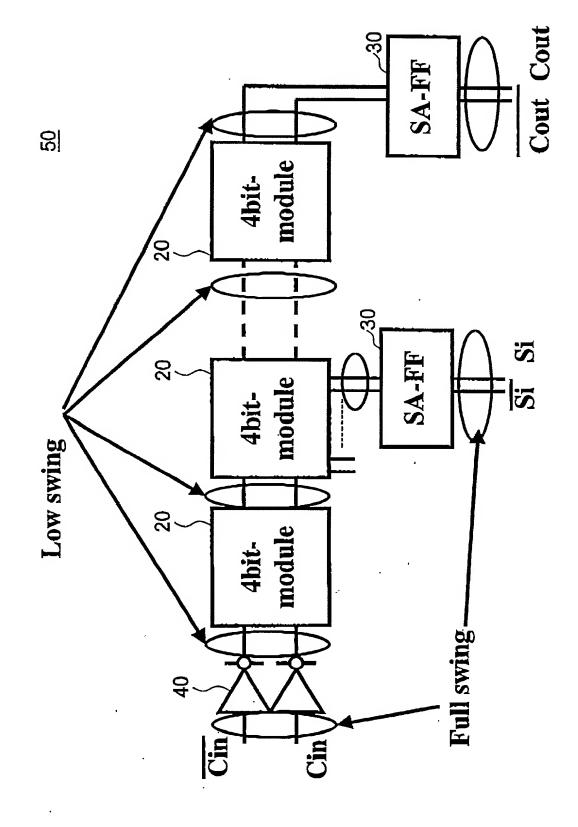
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**FIG.10** 





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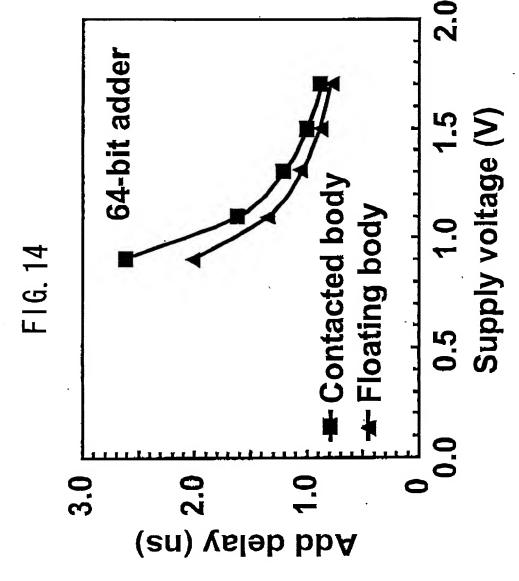


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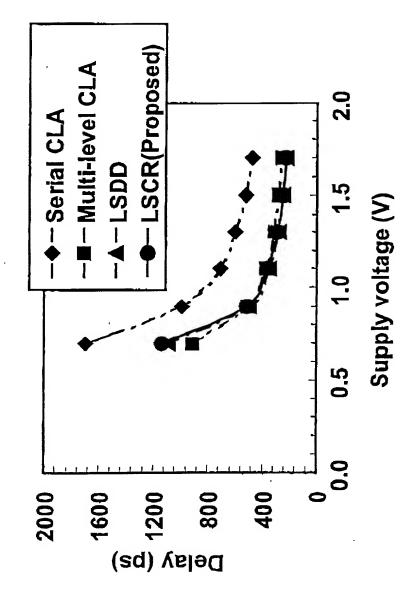
Floating body 64b adder 840 mm **Fest circuit Contacted body** 64b adder

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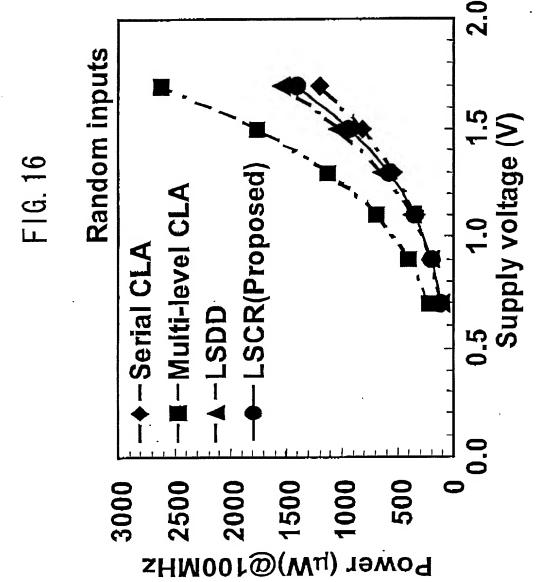


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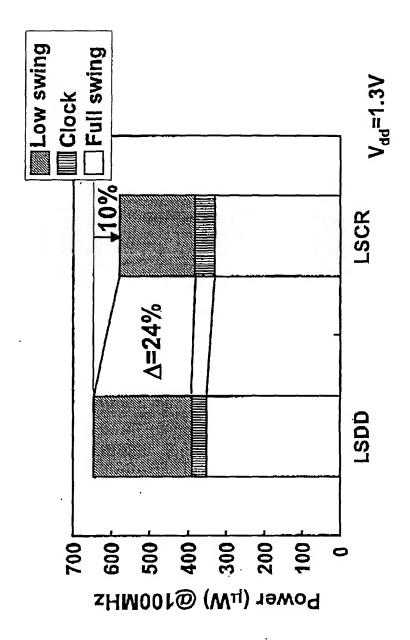


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